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Sheet 1 of 1

Form 1449*	Atty. Docket No.: 499.019US1	Serial No. 09/418,706
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Dennis Abts et al.	
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## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

MS	"Spec-Based Verification", <u>Verisity Designs, Inc.</u> , <a href="http://www.verisity.com/html.specbased.html">http://www.verisity.com/html.specbased.html</a> , pp. 1-17, (1999)
	Abts, D., et al., "Verifying Large-Scale Multiprocessore Using an Abstract Verification Environment", <u>Proceedings of the 36th Annual Design Automation Conference</u> , pp. 1-10, (1999)
	Chapiro, D., "The Next Frontier: High-Level Functional Verification", <u>Integrated Systems Design Magazine</u> , <a href="http://www.isdmag.com/Editorial/1997/Viewpoint9704.html">http://www.isdmag.com/Editorial/1997/Viewpoint9704.html</a> , pp. 1-2, (1997)
	Eiriksson, A., et al., "Origin System Design Methodology and Experience: 1M-gate ASICs and Beyond", <u>COMPCON-97</u> , Silicon Graphics, Inc., Mountain View, CA, pp. 1-8, (1997)
	Jones, K.D., et al., "The Automatic Generation of Functional Test Vectors for Rambus Design", <u>Proceedings of the 33rd Annual Design Automation Conference</u> , pp. 415-420, (1996)
↓	Subrahmanyam, P., "Functional verification finds a good friend in Vera", <u>TechWeb</u> , <a href="http://techweb.com/se/directlink.cgi?EET19970609S0083">http://techweb.com/se/directlink.cgi?EET19970609S0083</a> , pp. 1-4, (1997)

Examiner <u>Samuel Bruck</u>	Date Considered <u>8/23/2003</u>
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\*Substitute Disclosure Statement Form (PTO-1449)

\*\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.